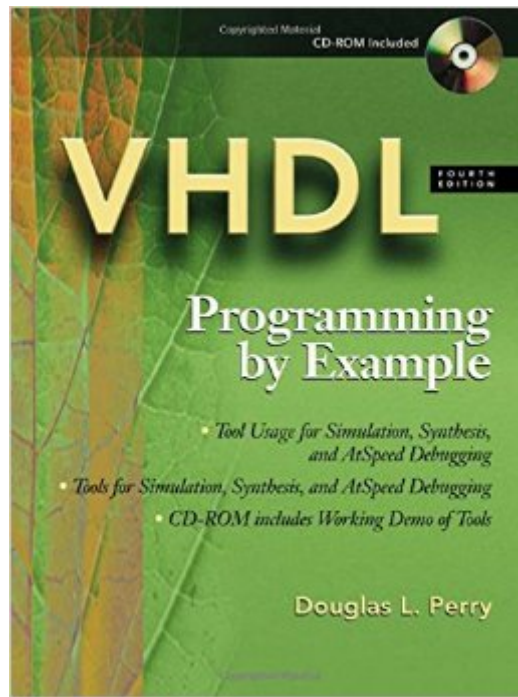


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VHDL : Programming By Example



Synopsis

THE HANDS-DOWN FAVORITE USER'S GUIDE TO VHDL COMPLETELY UPDATED TO REFLECT THE VERY LATEST DESIGN METHODS CD-ROM WITH WORKING CODE EXAMPLES, VERIFICATION TOOLS AND MORE No matter what your current level of expertise, nothing will have you writing and verifying concise, efficient VHDL descriptions of hardware designs as fast or as painlessly as this classic tutorial from master teacher Doug Perry. Beginners will find it an invaluable learning tool and experienced pros will keep it on their desk as a trusted reference. Perry teaches VHDL through a series of hundreds of practical, detailed examples, gradually increasing in complexity until you're capable of designing a fully functional CPU. The new Fourth Edition has been completely updated with all of the VHDL codes used in the examples changed to reflect today's faster and more efficient design methods. You'll also find:

- * A CD-ROM containing working code of all of the VHDL examples, with their matching designs along with VITAL verification tools and a working copy of ModelSIM
- * All the tools you need for simulation and synthesis
- * A listing of the IEEE 1164 STD-LOGIC package used throughout the book
- * Useful tables and figures
- * Instructions for reading the Backus-Naur format (BNF) descriptions found in the VHDL Language Reference Manual

There truly is no faster or smarter way to master VHDL than Doug Perry's "learn by example" approach. It works!

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EXAMPLES, VERIFICATION TOOLS AND MORE No matter what your current level of expertise, nothing will have you writing and verifying concise, efficient VHDL descriptions of hardware designs as fast as or as painlessly as this classic tutorial from master teacher Doug Perry. Beginners will find it an invaluable learning tool and experienced pros will keep it on their desk as a trusted reference. Perry teaches VHDL through a series of hundreds of practical, detailed examples, gradually increasing in complexity until you're capable of designing a fully functional CPU. The new Fourth Edition has been completely updated with all of the VHDL codes used in the examples changed to reflect today's faster and more efficient design methods. You'll also find: A CD-ROM containing working code of all of the VHDL examples, with their matching designs along with VITAL verification tools and a working copy of ModelSIM All the tools you need for simulation and synthesis A listing of the IEEE 1164 STD-LOGIC package used throughout the book Useful tables and figures Instructions for reading the Bachus-Naur format (BNF) descriptions found in the VHDL Language Reference Manual There truly is no faster or smarter way to master VHDL than Doug Perry's learn by example approach. It works!

Douglas L. Perry is Founder and VP of Customer Solutions at Bridges2Silicon a new startup HDL hardware debugging company. Prior positions include Director of Strategic Marketing with Exemplar Logic, Inc. Mr. Perry has been active in the CAE field for almost two decades and is also the author of the first three editions of VHDL Programming by Example. He earned his B.S. Degree in Electrical Engineering at South Dakota State University and also did graduate studies at the University of Santa Clara. He lives in San Ramon, California.

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